

10/819624
501.35478CX23**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant(s): H. MIWA, et al

Serial No.: Not yet assigned

Filed: March 29, 2004

For: CLOCK SYNCHRONIZED NON-VOLATILE MEMORY DEVICE

**INFORMATION DISCLOSURE STATEMENT
UNDER 37 CFR §1.97 & 1.98****MS DD**Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

March 29, 2004

Sir:

In the matter of the above-identified application, this Information Disclosure Statement is being submitted with the following citation as specified in 37 CFR §1.97(d).

"A copy of any patent, publication or other information listed in an Information Disclosure Statement is not required to be provided if it was previously cited by or submitted to the Office in a prior application, provided that the prior application is properly identified in the statement and relied upon for an earlier filing date under 35 U.S.C. §120."

Applicant(s) are submitting herewith a copy of Form PTO-1449 which list documents cited in parent application(s) Serial No. 10/373,737, filed February 27, 2003; which is a continuation of application Serial No. 10/223,485, filed August 20, 2002; Serial No. 10/223,220, filed August 20, 2002; which is a continuation of application Serial No. 10/020,873, filed December 19, 2001; which is a continuation of application Serial No.

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09/817,021, filed March 27, 2001, now U.S. Patent No. 6,366,495; which is a continuation of application Serial No. 09/583,949, filed May 31, 2000, now U.S. Patent No. 6,256,230; which is a continuation of application Serial No. 09/287,187, filed April 6, 1999, now U.S. Patent No. 6,111,790; which is a continuation of application Serial No. 09/053,494, filed April 2, 1998, now U.S. Patent No. 6,038,165; which is a continuation of application Serial No. 08/860,793, filed July 9, 1997, now U.S. Patent No. 5,889,698.

It is respectfully requested that this information disclosure statement be considered by the Examiner.

Please charge any shortage in the fees due in connection with the filing of this paper, including extension of time fees, to the deposit account of Antonelli, Terry, Stout & Kraus Deposit Account No. 01-2135 (501.35478CX23) please credit any excess fees to such deposit account.

Respectfully submitted,

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EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILE IF APPLICABLE
	4 9 6 4 0 7 9	10/90	Devin			
	5 3 6 5 4 8 6	11/94	Schreck			
	5 4 1 2 6 0 1	5/95	Sawada et al	365	185.03	
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	5 7 4 5 4 0 9	4/98	Wong et al	365	185.03	
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DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	ABSTRACT
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4 2 3 8 1 9 6	8/92	Japan			XX
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OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

Digest of Technical Papers, ATP 2.1: A 3.3V 128Mb Multi-level NAND Flash Memory for Mass Storage Applications, T. Jung, et al., ISSCC96/Session 2/Flash Memory/Paper TP 2.1, 1996 IEEE International State Circuits Conf.
Betty Prince, "Semiconductor Memories", 1983, Wiley, 2 nd Edition, pp. 620-621.

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	3 2 3 7 6 9 2	10/91	Japan			XX	
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	4 1 8 4 7 9 4	7/92	Japan			XX	
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	5 4 7 3 5 7 0	12/95	Sato et al	365	221	
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	6 3 6 6 4 9 5	4/02	Miwa et al	365	185.03	
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	6 0 4 4 0 3 1	3/00	Iadanza et al	365	230.03	

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0	7 0 5 7 4 8 4	3/95	Japan			XX
0	2 1 5 4 9 7		Japan			X
0	6 3 0 9 8 9 0	11/94	Japan			XX
0	1 2 3 8 4 2	9/79	Japan			
	1 1 3 4 7 9 3	5/89	Japan			x
	5 2 1 0 9 9 1	8/93	Japan			XX
	6 2 5 1 5 9 1	9/94	Japan			x
	7 9 3 9 7 9	4/95	Japan			XX
	2 0 4 0 1 9 8	2/90	Japan			XX

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	4 6 9 2 8 8 6	9/87	Miki et al	364	718	
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	5 5 3 9 6 8 8	7/96	Yiu, et al	365	185.14	
	5 5 4 4 0 9 9	8/96	Hara	365	185.19	
	5 6 7 7 8 6 9	10/97	Fazio, et al	365	185.03	
	5 6 1 5 1 5 3	3/97	Yiu, et al	365	185.19	

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